



### 2.0A SBR® SUPER BARRIER RECTIFIER **SMA**

## Product Summary (@ T<sub>A</sub> = +25°C)

| V <sub>RRM</sub> (V) | I <sub>O</sub> (A) | V <sub>F(MAX)</sub> (V) | I <sub>R(MAX)</sub> (µA) |
|----------------------|--------------------|-------------------------|--------------------------|
| 40                   | 2                  | 0.55                    | 500                      |

### **Features and Benefits**

- Low Leakage Current
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## Applications

- **SMPS**
- DC-DC Converter
- Freewheeling Diodes

### **Mechanical Data**

- Case: SMA
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208 (63)
- Polarity Indicator: Cathode Band
- Weight: 0.064 grams (Approximate)







## **Ordering Information** (Note 4)

| Part Number  | Case | Packaging        |
|--------------|------|------------------|
| SBR2A40SA-13 | SMA  | 5000/Tape & Reel |

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.</p>
  4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

## **Marking Information**



S Q 4 = Product Type Marking Code D!! = Manufacturer's Code Marking YWW = Date Code Marking Y = Last Digit of Year (ex: 9 for 2009) WW = Week Code (01 - 53)XX = Foundry and Assembly



## Maximum Ratings @T<sub>A</sub> = +25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

| Characteristic  | Symbol           | Value | Unit |
|---|------------------|-------|------|
| Peak Repetitive Reverse Voltage   | V <sub>RRM</sub> |       |      |
| Working Peak Reverse Voltage  | $V_{RWM}$        | 40    | V    |
| DC Blocking Voltage   | $V_{RM}$         |       |      |
| Average Rectified Output Current (See Figure 1)   | lo               | 2     | Α    |
| Non-Repetitive Peak Forward Surge Current 8.3ms<br>Single Half Sine-Wave Superimposed on Rated Load | I <sub>FSM</sub> | 15    | Α    |

## **Thermal Characteristics**

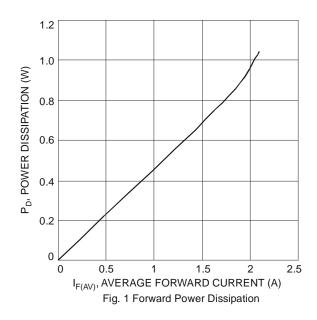
| Characteristic  | Symbol                            | Value       | Unit |
|---|-----------------------------------|-------------|------|
| Typical Thermal Resistance Junction to Ambient (Note 5) | $R_{\theta JA}$                   | 110         | °C/W |
| Operating and Storage Temperature Range                 | T <sub>J</sub> , T <sub>STG</sub> | -65 to +150 | °C   |

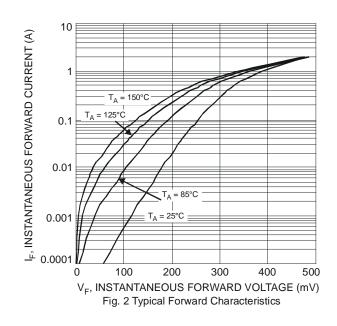
## Electrical Characteristics @TA = +25°C unless otherwise specified

| Characteristic           | Symbol | Min | Тур | Max  | Unit | Test Condition                                |
|--------------------------|--------|-----|-----|------|------|---|
| Forward Voltage Drop     | \/_    | -   | -   | 0.55 | I V  | I <sub>F</sub> = 2.0A, T <sub>J</sub> = +25°C |
|                          | VF     |     | -   | 0.50 |      | $I_F = 1.0A, T_J = +25$ °C                    |
| Leakage Current (Note 6) | _      | -   | -   | 500  | μA   | V <sub>R</sub> = 40V, T <sub>J</sub> = +25°C  |
|                          | IR     |     | -   | 100  | mA   | $V_R = 40V, T_J = +125$ °C                    |

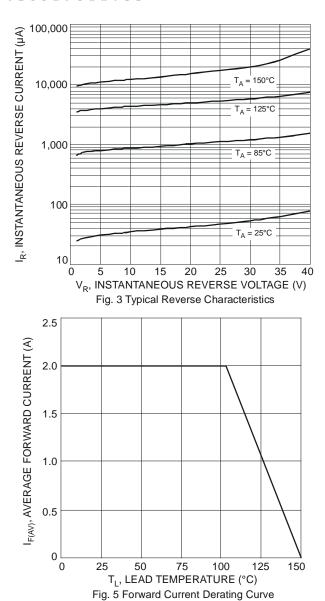
Notes:

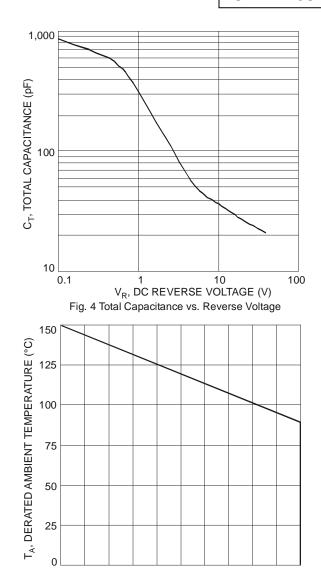
- 5. Device mounted on Polymide substrate, with 1" x 1", 2 oz. Copper, double-sided PCB board.
- 6. Short duration pulse test used to minimize self-heating effect.



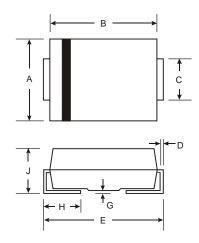








# **Package Outline Dimensions**



| SMA                  |      |      |  |
|----------------------|------|------|--|
| Dim                  | Min  | Max  |  |
| Α                    | 2.29 | 2.92 |  |
| В                    | 4.00 | 4.60 |  |
| С                    | 1.27 | 1.63 |  |
| D                    | 0.15 | 0.31 |  |
| Е                    | 4.80 | 5.59 |  |
| G                    | 0.05 | 0.20 |  |
| Н                    | 0.76 | 1.52 |  |
| J                    | 2.01 | 2.30 |  |
| All Dimensions in mm |      |      |  |

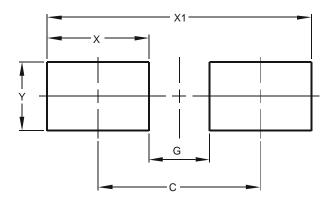
8 12 16 20 24 28 32  $\rm V_R,\,DC$  REVERSE VOLTAGE (V)

Fig. 6 Operating Temperature Derating

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## **Suggested Pad Layout**



| Dimensions | Value (in mm) |
|------------|---------------|
| С          | 4.00          |
| G          | 1.50          |
| Х          | 2.50          |
| X1         | 6.50          |
| Y          | 1.70          |

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